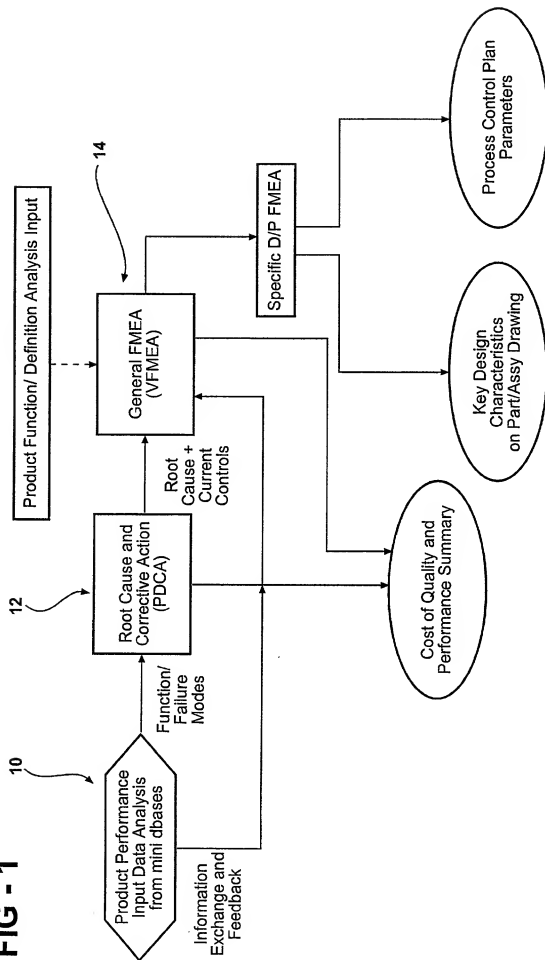
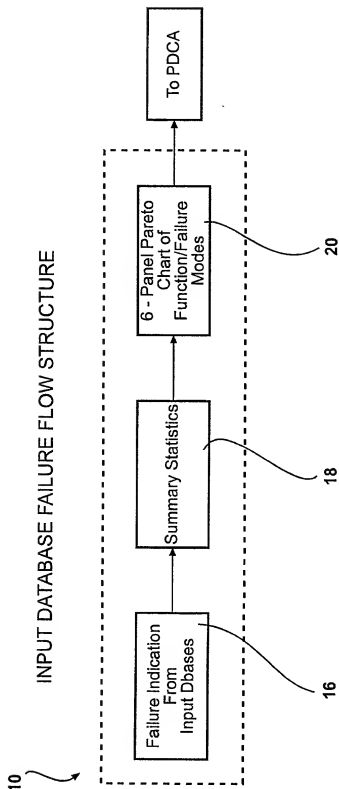


FIG - 1



208220-26258001

FIG - 2



208220-26258001

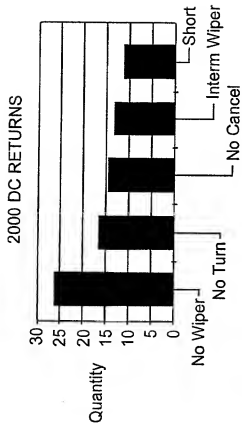


FIG - 3A

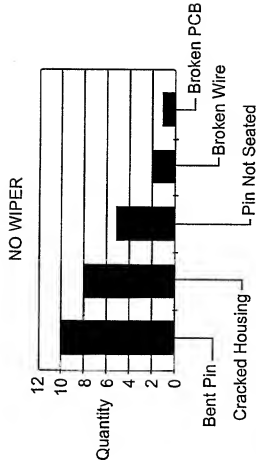


FIG - 3B

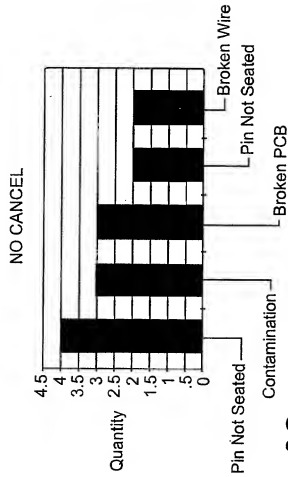


FIG - 3C

208220-26253001

NO TURN SIGNAL

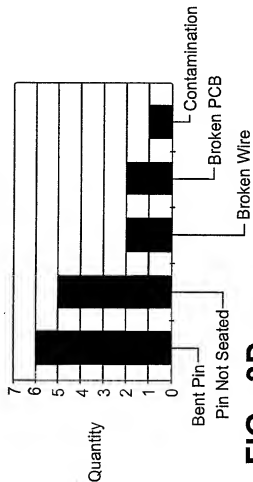


FIG - 3D

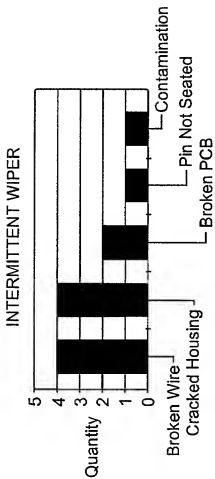


FIG - 3E

ELECTRICAL SHORT

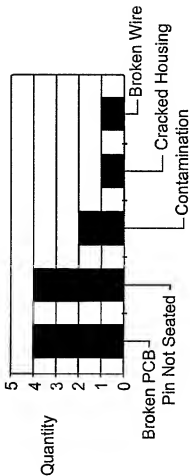


FIG - 3F

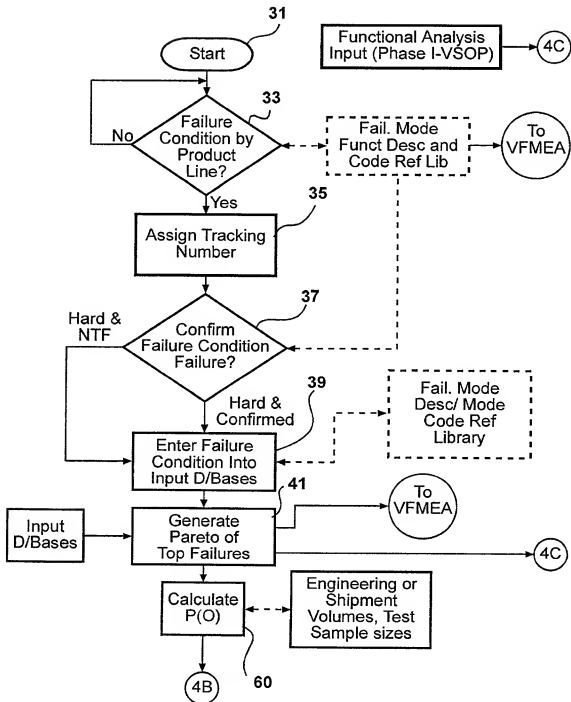


FIG - 4A

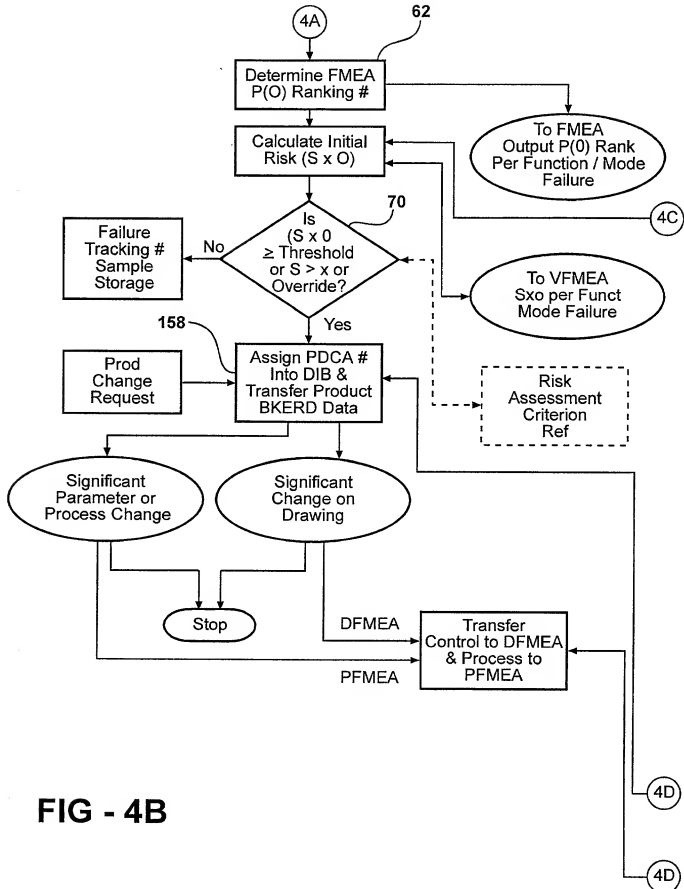


FIG - 4B

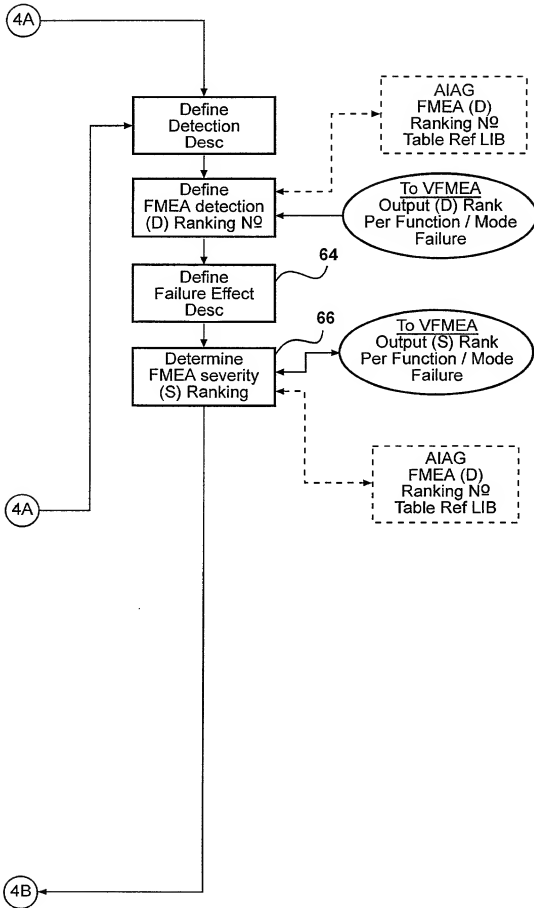


FIG - 4C

1005202-022802

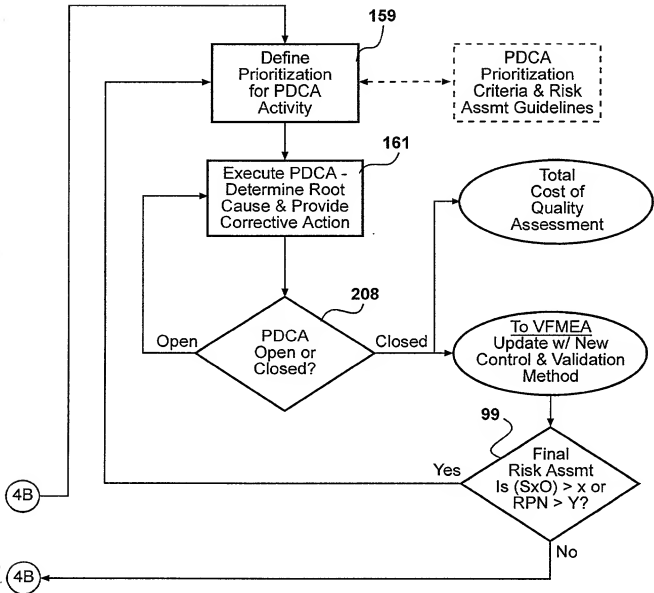


FIG - 4D

VFMEA (Overall Design / Process Risk Assessment) Process

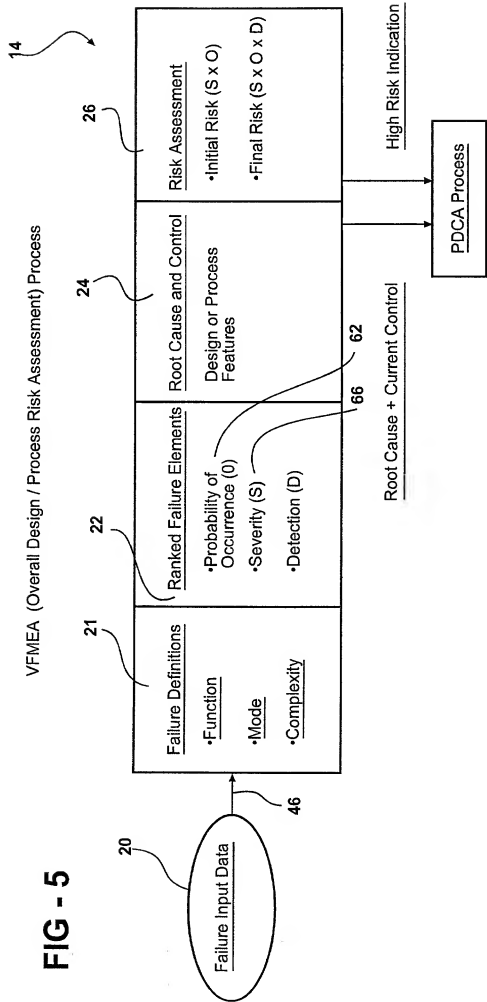


FIG - 5

20

Failure Input Data

46

21

22

24

26

14

Failure Definitions

• Function

• Mode

• Complexity

Ranked Failure Elements

• Probability of Occurrence (O)

• Severity (S)

• Detection (D)

Root Cause and Control

Design or Process Features

Risk Assessment

• Initial Risk (S x O)

• Final Risk (S x O x D)

Root Cause + Current Control

High Risk Indication

PDCA Process

VFMEA

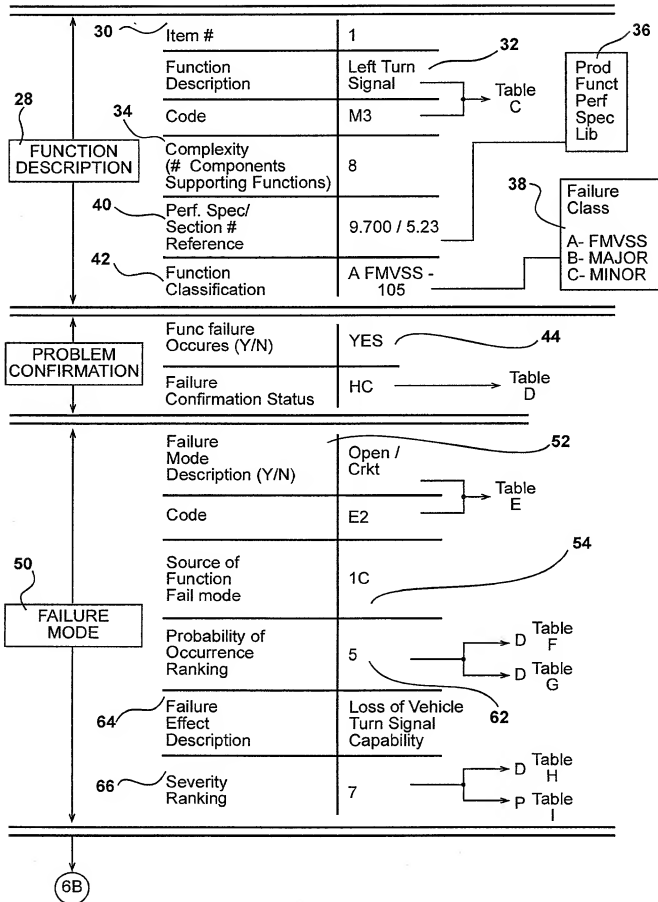


FIG - 6B

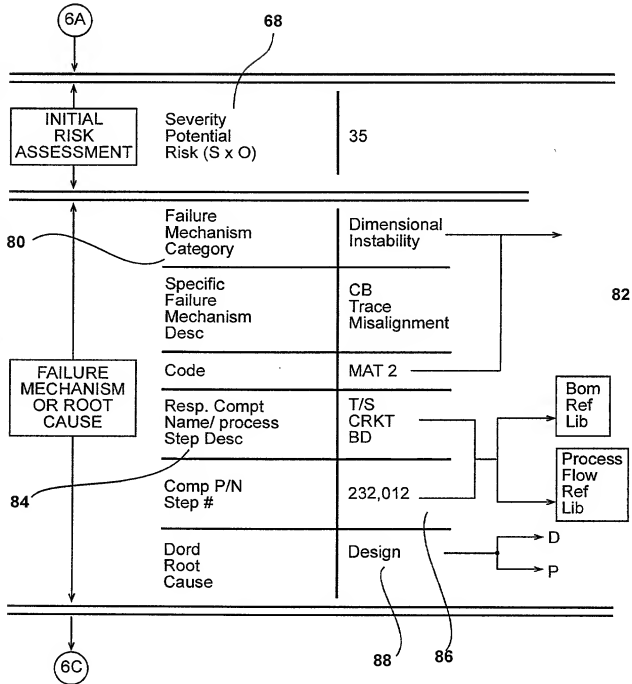


FIG - 6C

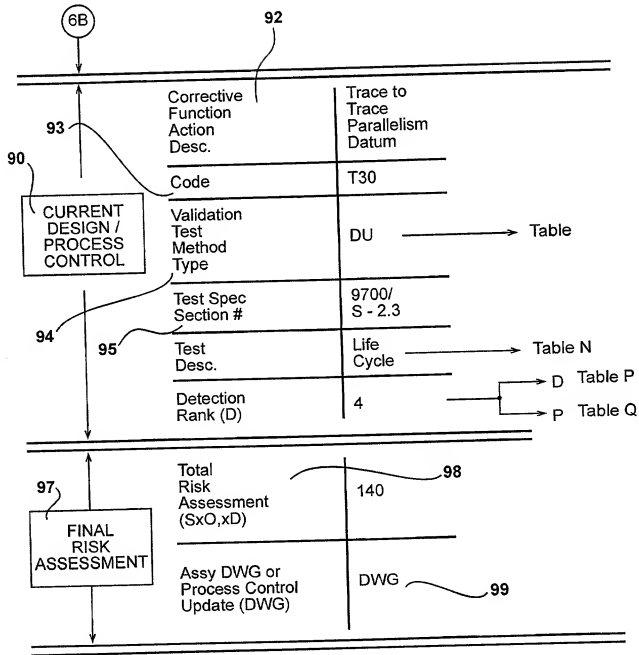


FIG - 7A

BACKGROUND DATA

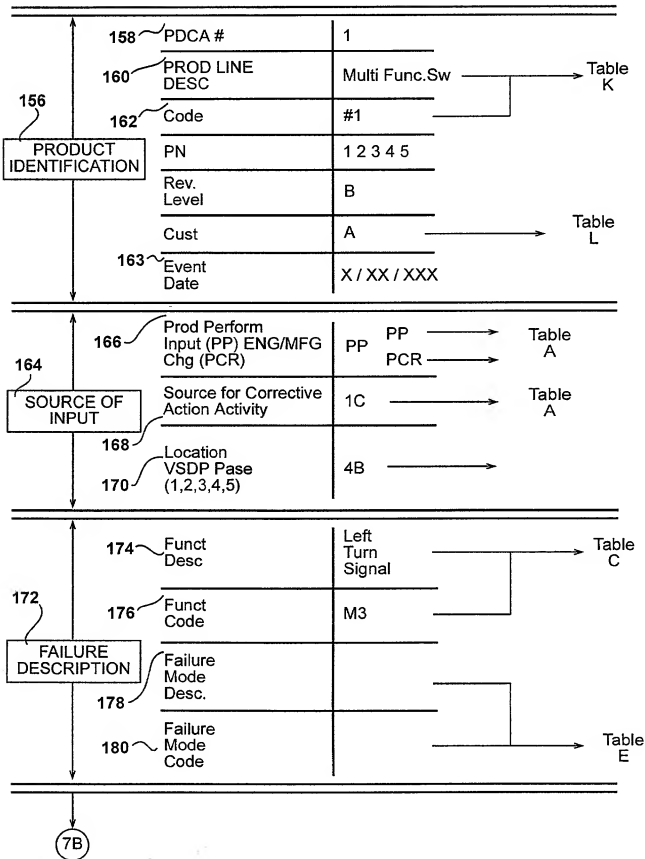


FIG - 7B

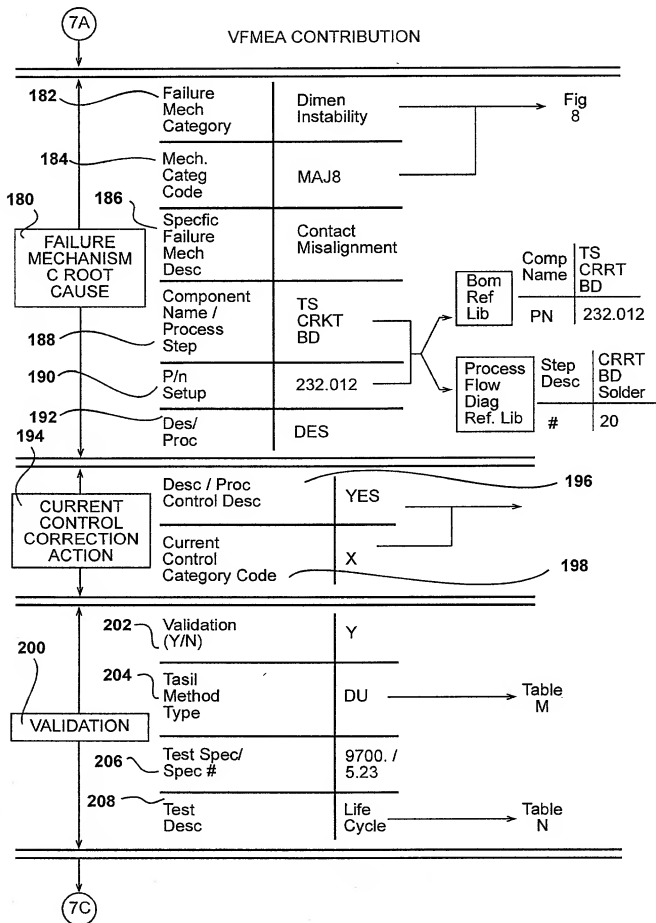


FIG - 7C

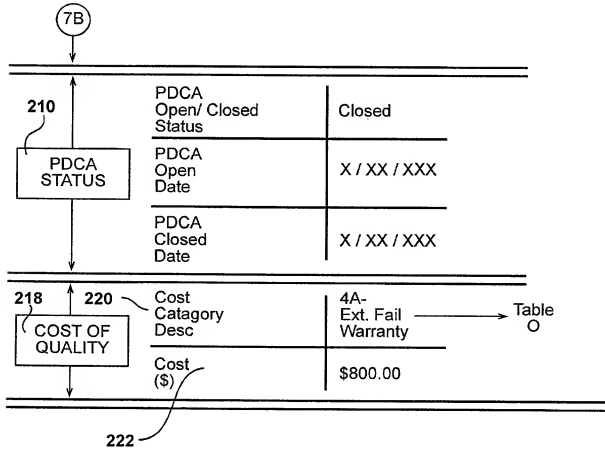


FIG - 8

Design and Process Failure Mechanism Categories

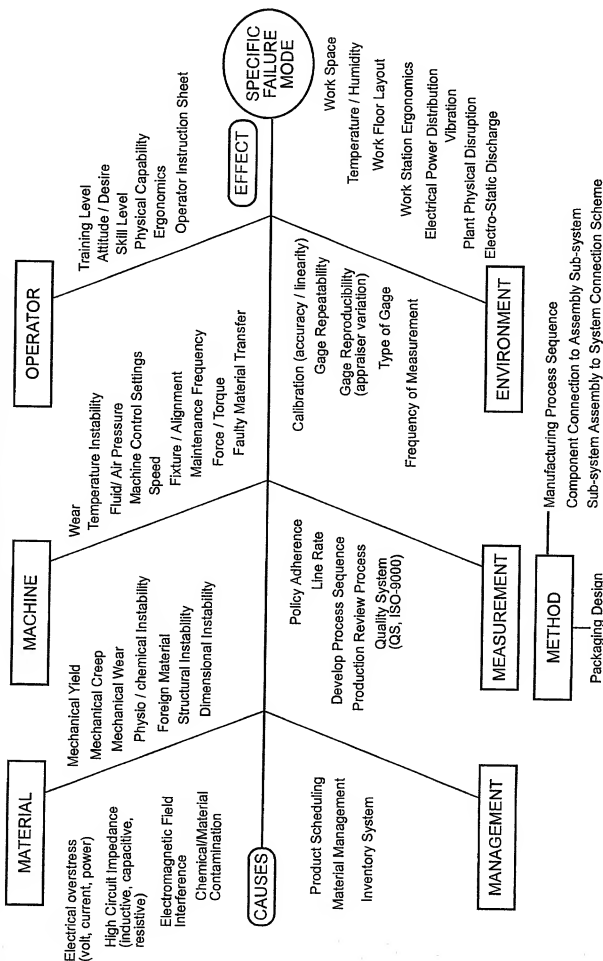


FIG - 9

